Translation

PATENT COOPERATION TREATY



PCT Rec'd PCT/PTO 10 NON 2005

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY (Chapter II of the Patent Cooperation Treaty)

(PCT Article 36 and Rule 70)

Applicant's or agent's file	reference		Total of the real real (0)				
JSONY-451PCT		FOR FURTHER ACTION		See Form PCT/IPEA/416			
International application No. PCT/JP2003/013832		International filin 29 October	g date (day/month/year) 2003 (29.10.2003)	Priority date (day/month/year)			
International Patent Classification (IPC) or national classification and IPC G11C 11/407 29 October 2003 (29.10.2003) 20 November 2002 (20.11.2002)							
Applicant							
SONY CORPORATION							
 This report is the international preliminary examination report, established by this International Preliminary Examining Authority under Article 35 and transmitted to the applicant according to Article 36. 							
2. This REPORT con	sists of a total of _	5she	ets, including this cover sh	neet			
J. Inis report is also	accompanied by AN	NEXES, compris	ing:				
a. sent to	the applicant and to	the International	Bureau) a total of	sheets, as follows:			
sheets of the description, claims and/or drawings which have been amended and are the basis of this report and/or sheets containing rectifications authorized by this Authority (see Rule 70.16 and Section 607 of the Administrative Instructions).							
sheets which supersede earlier sheets, but which this Authority considers contain an amendment that goes beyond the disclosure in the international application as filed, as indicated in item 4 of Box No. I and the							
b. (sent to the International Bureau only) a total of (indicate type and number of electronic carrier(s)) readable form only, as indicated in the Supplemental Box Relating to Sequence Listing (see Section 802 of the							
4. This report contains indications relating to the following items:							
Box No. I	Basis of the repor		tems:				
Box No. II	Priority	•					
Box No. III	•	it of opinion with r	egard to namely the state of	e step and industrial applicability			
Box No. IV	Lack of unity of ir	evention	cgald to hoverty, inventive	e step and industrial applicability			
Box No. V Reasoned statement under Article 35(2) with record to accord to							
citations and explanations supporting such statement Box No. VI Certain documents cited							
Box No. VII Certain defects in the international application							
Box No. VIII Certain observations on the international application							
Date of submission of the demand		Date of completion of this report					
24 March 2004 (24.03.2004)		20 March 2005 (20.03.2005)					
ame and mailing address of the IPEA/JP		Authorized officer					
acsimile No.		Telephone No.					
orm PCT/IPEA/409 (cover sheet) (January 2004)							

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.

PCT/JP2003/013832

Box No	D. I	Basis of the report					
1. With othe	n regard rwise ir	to the language, this report is based on the international application in the landicated under this item.	nguage in which it was filed, unless				
	This whic	report is based on translations from the original language into the following the is language of a translation furnished for the purpose of:	ng language,				
Į.	international search (under Rules 12.3 and 23.1(b))						
j		publication of the international application (under Rule 12.4)					
1	international preliminary examination (under Rules 55.2 and/or 55.3)						
		to the elements of the international application, this report is based on the receiving Office in response to an invitation under Article 14 are referr annexed to this report):	(replacement sheets which have been ed to in this report as "originally filed"				
		nternational application as originally filed/furnished					
		scription:					
	pages						
ŧ	pages'	received by this Authority on	, as originally filed/furnished				
	pages'						
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	pages*		, as originally filed/furnished				
	pages*	, as amended (tog	gether with any statement) under Article 19				
	pages*						
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		awings:					
	pages		, as originally filed/furnished				
	pages*	received by this Authority on					
	pages*	received by this Authority on					
	a seque	ence listing and/or any related table(s) - see Supplemental Box Relating to Se	quence Listing.				
3.	The an	nendments have resulted in the cancellation of:					
	$\prod t$	he description, pages					
		he claims, Nos.					
	=						
	H.	he drawings, sheets/figs					
	∺ "	he sequence listing (specify):					
	∟ a	ny table(s) related to sequence listing (specify):					
	(Rule 7	• • •	eport and listed below had not been indicated in the Supplemental Box				
	<u></u> ⊔ "	ne description, pages					
	ti	ne claims, Nos.					
	th	ne drawings, sheets/figs					
	th	ne sequence listing (specify):					
	ar	ny table(s) related to sequence listing (specify):					
·							
• If item	4 appli	es, some or all of those sheets may be marked "superseded."					

Claims

Claims

International application No.

PCT/JP03/13832

NO

YES

NO

Box No. V Reasoned state citations and e	tement under Article 35(2) with r explanations supporting such sta	J3/13832	
1. Statement	Providence and bottom sta	tement	
Novelty (N)	Claims	3-10, 12-14, 16-17	YES
Inventive step (IS)	Claims	1, 2, 11, 15	NO NO
. (= /	Claims	4-10	YES
		1-3, 11-17	NO

1-17

2. Citations and explanations (Rule 70.7)

Industrial applicability (IA)

Document 1: JP, 2001-135080, A (Matsushita Electric Industrial Co., Ltd.), 18 May, 2001 (18.05.01), paragraphs [0008], [0009] and [0013], Fig. 8, & US, 6349072, B1

Document 2: JP, 8-255107, A (Toshiba Corp.), 1 October, 1996 (01.10.96), full text, Fig. 3

Claims 1 and 2

The subject matters of claims 1 and 2 do not appear to be novel or to involve an inventive step, since they are described in document 1 (paragraphs [0008], [0009] and [0013] and Fig. 8) newly cited in the IPER. Claim 3

The subject matter of claim 3 does not appear to involve an inventive step in view of document 1 and document 2 cited in the ISR.

It is well known that the address of the memory cell in a request signal and the address of data temporarily held in an amplifier are compared to judge whether or not a page is hit.

Claims 4-10

The subject matters of claims 4-10 appear to involve an inventive step in view of documents 1 and 2.

Documents 1 and 2 do not describe that when the address of a requested memory cell and the address of data of an amplifier are compared and found not to agree with each other, an instruction is given to collectively process charge storing action, charge taking out action and input/output action at a time with a single clock used as synchronous timing. This constitution exhibits an advantageous effect that highly efficient data processing

Claims 11 and 15

The subject matters of claims 11 and 15 do not appear to be novel or to involve an inventive step, since they are described in document 2 (Fig. 3).

Claims 12-14, 16 and 17

The subject matters of claims 12-14, 16 and 17 do not appear to involve an inventive step in view of documents 1 and 2.

As described in document 1 (paragraphs [0008], [0009] and [0013] and Fig. 8), a person skilled in the art could have easily combined a read/write cycle and a pre-charge cycle into one cycle in the case where the

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International application No.

Box No. VI Certain document	ATENTABILITY	PCT/JP03/13832						
Certain published documents (Rule 70.10)								
Application No. Patent No.	Publication date (day/month/year)	Filing date (day/month/year)	Priority date (valid claim) (day/month/year)					
JP 2004-259318 A [E, X]	16.09.2004	24.02.2003						
			·					
		-						
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2. Non-written disclosures (Rule 70 Kind of non-written disc	closure Date of non-w	D written disclosure referr conth/year)	Date of written disclosure ring to non-written disclosure (day/month/year)					

INTERNATIONAL PRELIMINARY REPORT ON PATENTABILITY

International application No.

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Box No. VIII Certain observations on the international application

The following observations on the clarity of the claims, description, and drawings or on the question whether the claims are fully

- 1. Claim 4 describes "in the order of ... the input/output action of the said amplifier for the outside of the said information storage and the charge taking out action for taking out the charges existing in the range from the said memory cell to the said amplifier," but this order is considered to be reverse. This applies also to claim 8.
- 2. In claim 8, "when ... agrees with" is considered to be an error for "when ... does not agree with."
- 3. Claim 10 describes as if the order of the input/output action and the charge storing action is changed in response to the frequency of a synchronous clock, but "The Best Modes for Carrying Out the Invention" of the present application describes to the effect that whether the input/output action and the charge storing action are carried out in different cycles or in one cycle is selected in response to the frequency of a synchronous clock. The descriptions do not match.

Form PCT/IPEA/409 (Box No. VIII) (January 2004)